

Ministry of Higher Education and Scientific Research

Al-Furat Al- Awsat Technical University

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Lecture Outlines :

- **Introduction**
- **Error detection codes**
 - **Error detection codes / parity codes**
- **Error correction codes**
 - **Error correction codes / Basic definitions**
 - **Error Correction Codes / Hamming Codes**
 - **Error Correction Codes / Cyclic Codes**

Channel Coding: To ensure reliable communications, techniques have been developed that allow bit errors to be detected and corrected. The process of error detection and correction involves adding extra redundant bits to the data to be transmitted. This process is generally referred to as channel coding.

Channel coding methods fall into two separate categories:

- **Error detection codes:** only have the ability to confirm that bit error(s) has occurred, however they cannot tell you which bit was in error. To fix the error, the receiver must request a retransmission.
- **Error correcting codes or forward error correction (FEC) codes:** have the ability to detect some bit errors and fix them without requiring a retransmission.

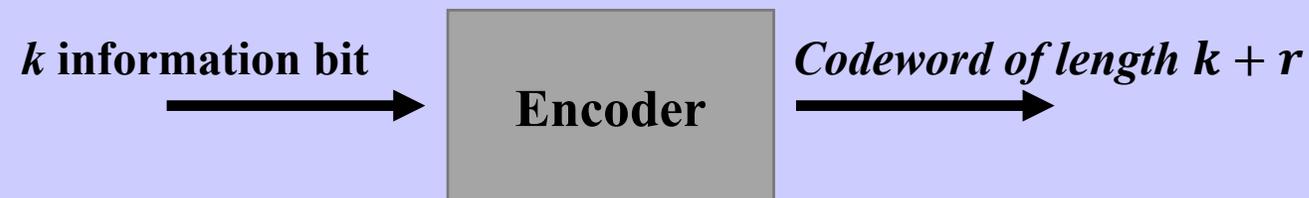
Block Codes In block codes, the encoder takes in k information or message bits and produces a codeword of length $n = k + r$. Since $k + r > k$, the quantity r represents the number of extra bits (redundancy) added. This would be referred to as an (n, k) linear code.

$$n = k + r$$

k : bit in data

r : redundant bit

n = codeword bit



Example 1: Linear block code (7,4) with $k=4$ and $k+r=7$

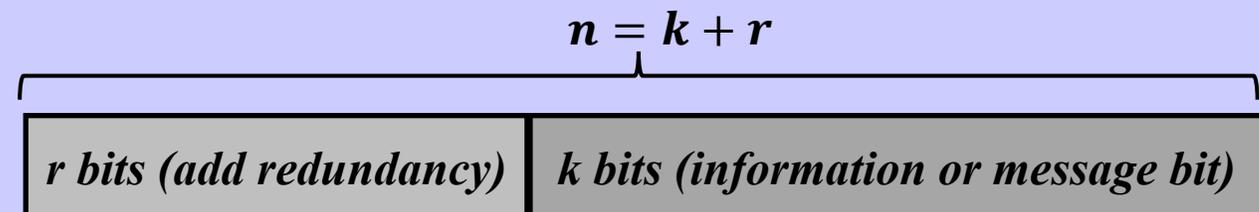
Message	Code words
(0 0 0 0)	(0 0 0 0 0 0 0)
(0 0 0 1)	(1 0 1 0 0 0 1)
(0 0 1 0)	(1 1 1 0 0 1 0)

A key point about channel coding is that there is a cost to be paid of increasing reliability. The extra n bits added by encoding result in:

- Larger file sizes for storage.
- Higher required transmission data rates.

This cost is represented by the code rate. The code rate R_c is the ratio of the number of information bits k to the number of bits in the codeword $k+r$.

$$R_c = k/n$$



Example 2: code (8,7)

That is mean $n = 8\text{bit}$, $k = 7\text{bit}$, and $r = 8 - 7 = 1\text{bit}$

$$R_c = k/n = 7/8$$

Simple Error Detection Codes:

Parity codes The simplest kind of error-detection code is the *parity code*. To construct an even-parity code, add a parity bit such that the total number of 1's is even.

Information bits	Even parity code	Odd parity code
000	000 0	000 1
001	001 1	001 0
010	010 1	010 0
011	011 0	011 1

Parity check code (n , k)

$$R_c = k/(k + 1)$$

1-bit parity codes can **detect** single bit errors, but they **do not detect** 2 bit errors.

Probability (detecting errors)= Probability (odd number of errors) and

Probability (undetected errors)= Probability (even number of errors).

Example 3: Parity check code of $k=7$ bit. Calculate P (undetected error) and P (detected error)

Solution:

$$P(\text{undetected error}) = \sum C_k^n p^k (1-p)^{n-k} = C_2^8 p^2 (1-p)^6 + C_4^8 p^4 (1-p)^4 + C_6^8 p^6 (1-p)^2 + C_8^8 p^8 \approx 28 \times 10^{-4}$$

The probability of detected errors will be:

$$P(\text{detect error}) = \sum C_k^n p^k (1-p)^{n-k} = C_1^8 p^1 (1-p)^7 + C_3^8 p^3 (1-p)^5 + C_5^8 p^5 (1-p)^3 + C_7^8 p^7 (1-p) \approx 8 \times 10^{-3}$$

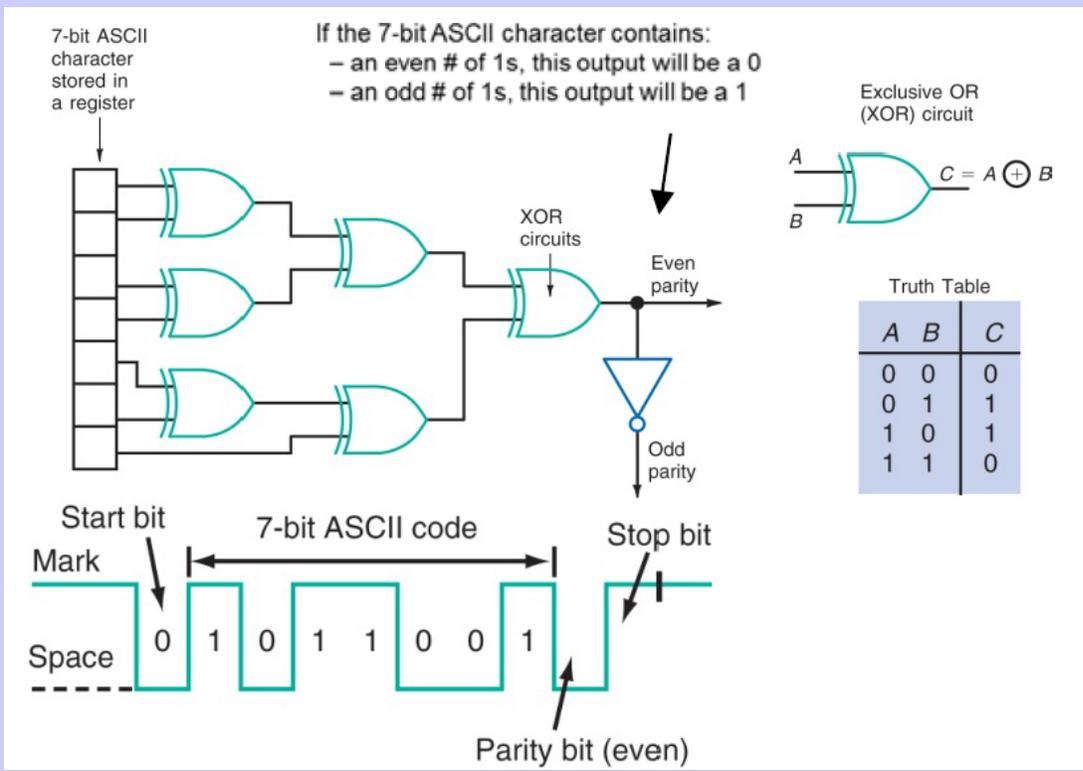
Homework1: assume there are $n=12$ bits in a codeword (packet). Probability of error in a single bit transmission $p_b=10^{-3}$. Find the probability of error-detection failure.

Lecture 6: Channel Coding

Error detection codes / parity codes

To implement these parity generators, simple Ex-OR gates are used at TX and RX as shown below

Information bits	Even parity code
1010111	1
0110101	0



Example 4: Parity check code (8,7) find code word if data $I_1 = [0111010]$, $I_2 = [1100111]$, and $I_3 = [0000000]$.

Solution:

$$C_i = [I_i: \text{parity bit}]$$

$$C_1 = [01110100]$$

$$C_2 = [11001111]$$

$$C_3 = [00000000]$$

Excercise 1: detect error in the received code word (use parity check code (8,7) then find data words.

$$C_1 = [01010101]$$

$$C_2 = [00000001]$$

$$C_3 = [11110110]$$

Solution:

- In code C_1 number of ones is even there is no error, data is **0101010**
- In code C_2 number of ones is odd there is error
- In code C_3 number of ones is even there is no error, data is **1111011**

Systematic and nonsystematic codes: If information bits (a 's) are unchanged in their values and position at the transmitted codeword, then this code is said to be systematic.

Input data $[D] = [a_1 a_2 a_3 \dots \dots a_k]$,

Output systematic (n,k) codeword is $[C] = [a_1 a_2 a_3 \dots \dots a_k c_1 c_2 c_3 \dots \dots c_r]$

However if data bits are spread or changed at the output codeword then, the code is said to be nonsystematic:

Output nonsystematic $(7,4)$ codeword is $[C] = [c_2 a_1 c_3 a_2 c_1 a_4 a_3]$

Hamming distance: The ability of error detection and correction codes depends on this parameter. The Hamming distance between two codewords c_i and c_j is denoted by d_{ij} which is the number of bits that differ. For a binary (n,k) code with 2^k possible codeword then the minimum Hamming distance (HD) is the $\min(d_{ij})$ of course $n \geq d_{ij} \geq 0$

Example 5: Find the Hamming distance between the two codewords:

$C_1 = [1011100]$ and $C_2 = [1011001]$.

Solution: Here, the number of bits that differ is 2, hence $d_{12} = 2$

Homework 2: Find the minimum Hamming distance for the 3 codewords.

$$C_1 = [1011100], C_2 = [1011001], C_3 = [1011000]$$

Hamming weight: This is the number of 1's in the non zero codeword c_i . It is denoted by w_i . As will be shown later, and for linear codes, $w_{min} = HD = \min(d_{ij})$. This simplifies the calculation of HD. As an example 5,

Example 6: If $C_1 = [1011000]$, then $w_i = 3$, for $C_2 = [0001010]$, then $w_i = 2$ and so on.

Linear and non Linear codes: when the parity bits are obtained from a linear function of the k information bits then the code is said to be linear, otherwise it is a nonlinear code.

Hamming Bound: The purpose of Hamming bound is either

- 1) to choose the number of parity bits (r) so that a certain error correction capability is obtained. Or
- 2) to find the error correction capability (t) if the number of parity bits (r) is known for binary codes, this is given by:

$$2^{n-k} = 2^r \geq \sum_{j=0}^t C_j^n$$

where t is the number of corrected bits.

$$t = \text{int}\left[\frac{HD - 1}{2}\right]$$

$$\text{no. of detected error bit} = HD - 1$$

Example 7: for a single correction code with k=4 find the no. of parity bits that should be added.

Solution:

$$2^r \geq \sum_{j=0}^1 C_j^{4+r} = C_0^{4+r} + C_1^{4+r} \text{ This gives } 2^r \geq 1 + (4+r) \text{ and the minimum } r \text{ is } r=3$$

(take minimum r to have max code efficiency). This is the (7,4) code. the code is said to be perfect code.

Homework 3: if k=5 and up to 3 errors are to be corrected, find the no. of check bits that should be added.

Note: If the (n,k) codewords are trans. through a channel having error prob= p_e , then prob. of decoding a correct word at the Rx for t -error correcting code will be:

$P(\text{correct words})=p(\text{no error})+p(1 \text{ error})+\dots+p(t \text{ errors})$

and $\text{prob}(\text{erroneous word})=1-P(\text{correct word})$.

Excercise 2: If Code of $t = 2, n = 31$ find k

Solution: $n = 31$ $k = ?$ $r = ?$

$$2^r \geq \sum_{j=0}^t C_j^n$$

$$2^r \geq C_0^{31} + C_1^{31} + C_2^{31}$$

$$2^r \geq 1 + 31 + \frac{31 \cdot 30}{2}$$

$$2^r \geq 497 \Rightarrow r \geq 8.95$$

$$n=31 \quad r=9 \quad k=22\text{bit}$$

Code (31,22)

$$t = 2 \Rightarrow t = \text{int} \left[\frac{HD-1}{2} \right] \Rightarrow HD = 5$$

No. of detected error = $(HD - 1) = 4\text{bit}$

$$P(\text{correct error}) = \sum_{i=0}^t C_i^n P^i (1-P)^{n-i}$$

$$P(\text{correct error}) = C_0^{31} P^0 (1-P)^{31} + C_1^{31} P^1 (1-P)^{30} + C_2^{31} P^2 (1-P)^{29}$$

Example 8: Block code (7, 4) calculate no. detect and correct bit Hamming Distance (HD) and P(correct error).

Solution: $n = 7$ $k = 4$ $r = 7 - 4 = 3$ bit

$$2^r \geq \sum_{j=0}^t C_j^n$$

$$2^3 \geq C_0^7 + C_1^7 + C_2^7 \dots$$

$$8 \geq 1 + 7 + \frac{6 * 7}{2} \dots$$

Taken two element that mean $t = 1$

$$t = \text{int} \left[\frac{HD - 1}{2} \right] \Rightarrow \Rightarrow HD = 3$$

No. of detected error = $(HD - 1) = 2$

$$P(\text{correct error}) = \sum_{i=0}^t C_i^n P^i (1 - P)^{n-i}$$

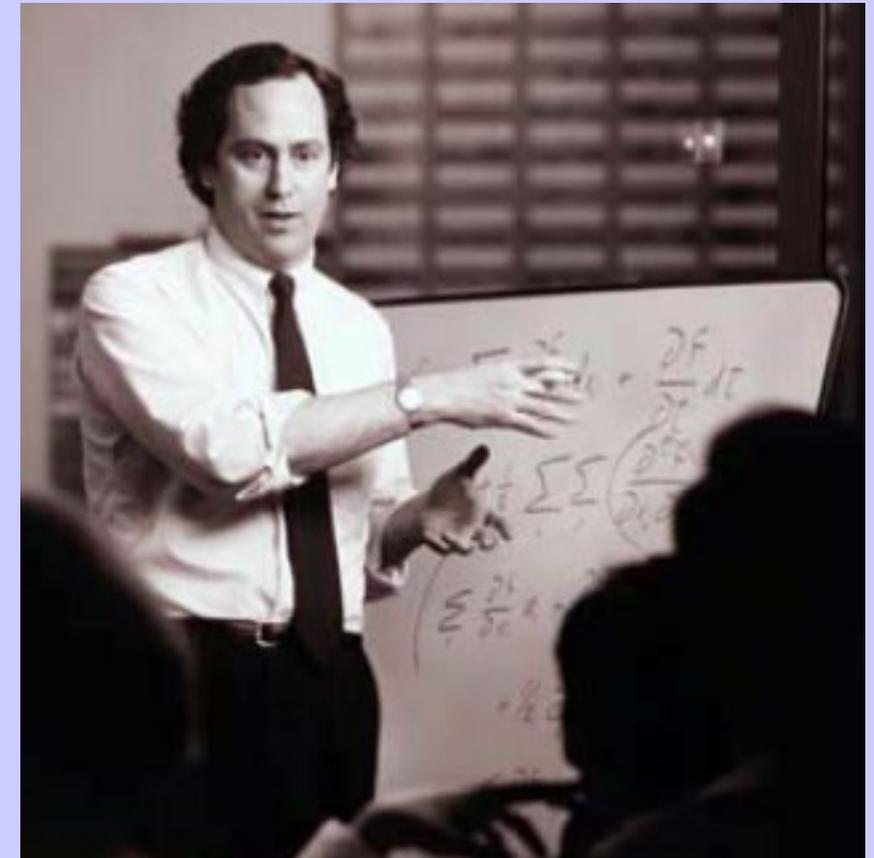
$$P(\text{correct error}) = C_0^7 P^0 (1 - P)^7 + C_1^7 P^1 (1 - P)^6$$

$$= 1 * 1(1 - P)^7 + 7P(1 - P)^6$$

$$\text{Code rate} = \frac{k}{n} = \frac{4}{7}$$

One way to detect and correct errors is to add parity checks to the codewords:

- **If we add a parity check bit at the end of each codeword we can detect one (but not more) error per codeword.**
- **By clever use of more than one parity bits, we can actually identify where the error occurred and thus also correct errors.**
- **Designing ways to add as few parity bits as possible to correct and detect errors is a really hard problem.**



Richard W. Hamming (11.2.1915-7.1.1998)

$$[H][C]^T = [0] \dots \dots \dots (2)$$

where: $[C] = [a_1 a_2 a_3 \dots a_k c_1 c_2 c_3 \dots c_r]$ and $[H]$ matrix is in fact related with $[G]$ matrix by:

$[H] = [-P^T : I_r]$, and for binary coding this – sign drops out. This rxn $[H]$ matrix is called the parity check matrix. As will be shown, encoding can be done either using eq(1) ($[G]$ matrix) or eq(2) ($[H]$ matrix), but decoding is done using $[H]$ matrix only.

Example 9: a given binary (7,4) Hamming code with a parity check matrix:

$$G = \begin{bmatrix} 1000011 \\ 0100101 \\ 0010110 \\ 0001111 \end{bmatrix}$$

$$G = [I_{k \times k} : P_{k \times r}]$$

Find: 1) no. of error correction and detect capability 2) Code rate 3) encoder circuit 4) if data [1011] find codewords.

Solution: $n=7$ $k=4$ $r=3$

$$2^r \geq \sum_{j=0}^t C_j^7$$

$$t = 1, HD = 3$$

$$\text{No. of detect error} = HD - 1 = 2$$

From example 8

$$C = D \cdot G$$

$$C = [1011] \cdot \begin{bmatrix} 1000\mathbf{011} \\ 0100\mathbf{101} \\ 0010\mathbf{110} \\ 0001\mathbf{111} \end{bmatrix}$$

$$C = [1011\mathbf{010}]$$

$$C = [I_1 I_2 I_3 I_4 C_1 C_2 C_3]$$

$$C_1 = I_2 \oplus I_3 \oplus I_4$$

$$C_2 = I_1 \oplus I_3 \oplus I_4$$

$$C_3 = I_1 \oplus I_2 \oplus I_4$$

Lecture 6: Channel Coding

Error Correction Codes / Hamming Codes

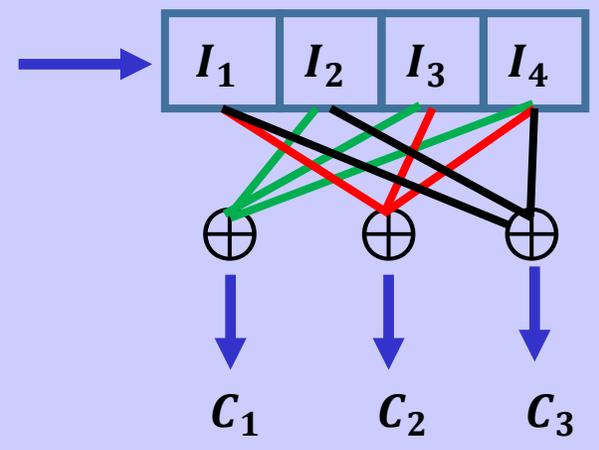
$$C_1 = I_2 \oplus I_3 \oplus I_4$$

$$C_2 = I_1 \oplus I_3 \oplus I_4$$

$$C_3 = I_1 \oplus I_2 \oplus I_4$$

Codeword Truth Table:

Data 4 bits	Codeword 7 bits
$I_1 I_2 I_3 I_4$	$I_1 I_2 I_3 I_4 C_1 C_2 C_3$
0000	0000000
0001	0001111
0010	0010
0011	0011
...	...
...	...
1111	1111



If codeword [0011001] find received signal

$$H = [P_{rxk}^T : I_{rxr}]$$

$$H = \begin{bmatrix} 0111100 \\ 1011010 \\ 1101001 \end{bmatrix}$$

$$H \cdot C^T = \begin{bmatrix} 0111100 \\ 1011010 \\ 1101001 \end{bmatrix} \cdot \begin{bmatrix} 0 \\ 0 \\ 1 \\ 1 \\ 0 \\ 0 \\ 1 \end{bmatrix} = \begin{bmatrix} 0 \\ 0 \\ 0 \end{bmatrix}$$

It is correct codeword data is [0011]

If codeword [0010001] find received signal

$$H \cdot C^T = \begin{bmatrix} 0111100 \\ 1011010 \\ 1101001 \end{bmatrix} \cdot \begin{bmatrix} 0 \\ 0 \\ 1 \\ 0 \\ 0 \\ 0 \\ 1 \end{bmatrix} = \begin{bmatrix} 1 \\ 1 \\ 1 \end{bmatrix}$$

There is error [0010001] correct codeword [0011001]

Homework 4: The generator matrix of a LBC is given by:

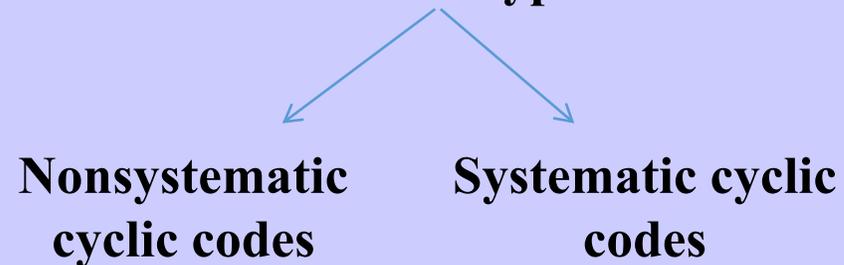
$$[G] = \begin{bmatrix} 1 & 0 & 0 & 1 & 1 & 0 & 1 & 1 & 0 & 0 \\ 0 & 1 & 0 & 0 & 1 & 1 & 0 & 1 & 1 & 0 \\ 0 & 0 & 1 & 0 & 0 & 1 & 1 & 1 & 0 & 1 \end{bmatrix}$$

a-Use Hamming bound to find error correction capability. b-Find the parity check matrix. c-find the code table, Hamming weight and the error correction capability then compare with part(a). d-If the received word is $[R]=[1011110011]$, find the corrected word at the Rx.

Cyclic Codes

These are subclass from the linear block codes. The name cyclic comes from the fact that any cyclic shift of a codeword is another codeword. i.e, if $[C1]=[0011010]$ is a codeword then $[C2]=[0001101]$ is another codeword obtained from $[C1]$ by a right circular shift.

Cyclic codes can be classified into two types:



Generation of cyclic codes:

A) nonsystematic Cyclic Codes: (Multiplicative):

Procedure:

(1) For $[D]=[a_1 a_2 \dots a_k]$ data word, write the data word in terms of a power of a dummy variable x with a_1 weighted as MSB (Most Significant Bit) and a_k as LSB (Least Significant Bit).

$$\begin{array}{cccccc} x^{k-1} & x^{k-2} & x^2 & x^1 & x^0 & \\ a_1 & a_2 & \dots & a_{k-2} & a_{k-1} & a_k \end{array}$$

MSB

LSB

$D(x)=a_k + a_{k-1}x + a_{k-2}x^2 + \dots + a_2x^{k-2} + a_1x^{k-1}$ where "+" sign is mod-2 addition (EX-OR)

For example if $[D]=[1 1 1 0 1]$,

then $D(x)=1+x^2+x^3+x^4$

and if $D(x)=x^6+x^2+1$ then $[D]=[1000101]$

2) Multiply $D(x)$ by what is called generator polynomial $g(x)$ of order $r=n-k$.

(3) The output codeword polynomial will be:

$C(x)=D(x)g(x)$ from which we can find the output codeword $[C]$

Example: $g(x) = x^3 + x + 1$

A.

1. $x^r = x^3$
2. have $x^r, 1$

B.

$D = [1\ 0\ 1\ 1]$

MSB

LSB

$D = [1\ 0\ 0\ 0]$

$$D(x) = 1 + 1x + 0x^2 + 1x^3$$

$$D(x) = 1 + 1x + 1x^3$$

$$D(x) = x^3$$

Example 10: Find codeword using nonsystematic cyclic code, if $g(x) = x^3 + x + 1$ and $D = [0\ 0\ 1\ 1]$

Solution:

$$1. \quad G(x) = x^3 + x + 1$$

$$r=3$$

$$N = k + r = 4 + 3 = 7$$

$$2. \quad D = [0\ 0\ 1\ 1]$$

$$D(x) = 1 + x \implies k=4$$

Code (7,4) , code rate = 4/7

$$C(x) = G(x)D(x)$$

$$= (x + 1)(x^3 + x + 1)$$

$$= [x^4 + x^2 + x + x^3 + x + 1]$$

$$C(x) = [0011101]$$

Homework 5: Find codeword using nonsystematic cyclic code, if $g(x) = x^4 + x^2 + 1$ and $D = [1\ 0\ 0\ 1]$