



Integrated Circuits Design by FPGA

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Objectives of this Lecture

- To define **Procedure**
- To implement **Procedure** in examples design.

Contents of this Lecture

- Introduction
- Procedure
- Procedure Location
- FUNCTION versus PROCEDURE Summary

Introduction

- Functions and Procedures are collectively called subprograms.
- From a construction point of view, they are very similar to a PROCESS. For they are the only pieces of sequential VHDL code, and thus employ the same sequential statements seen there (IF, CASE, and LOOP; WAIT is not allowed).
- However, from the applications point of view, there is a fundamental difference between a **PROCESS** and a **FUNCTION** or **PROCEDURE**. While the first is intended for immediate use in the main code, the others are intended mainly for LIBRARY allocation, that is, their purpose is to store commonly used pieces of code, so they can be reused or shared by other projects.

- A **PROCEDURE** is very similar to a FUNCTION and has the same basic purposes. However, a procedure can return more than one value.
- Like a FUNCTION, two parts are necessary to construct and use a PROCEDURE: the procedure itself (**procedure body**) and **a procedure call.**

Procedure Body

```
PROCEDURE procedure_name [<parameter list>] IS
    [declarations]
BEGIN
    (sequential statements)
END procedure_name;
```

In the syntax above, <parameter list> specifies the procedure's input and output parameters; that is:

```
(parameter list) = [CONSTANT] constant_name: mode type;
```

```
\langle \text{parameter list} \rangle = \text{SIGNAL signal_name: mode type; or}
```

```
\langle \text{parameter list} \rangle = \text{VARIABLE variable_name: mode type};
```

- A **PROCEDURE** can have any number of IN, OUT, or INOUT parameters, which can be SIGNALS, VARIABLES, or CONSTANTS.
- For input signals (mode IN), the default is CONSTANT, whereas for output signals (mode OUT or INOUT) the default is VARIABLE.
- As seen before, WAIT, SIGNAL declarations, and COMPONENTS are not synthesizable when used in a FUNCTION. The same is true for a **PROCEDURE**, the exception that a SIGNAL can be declared, but then the PROCEDURE must be declared in a PROCESS.

PROCEDURE my_procedure (a: IN BIT; SIGNAL b, c: IN BIT; SIGNAL x: OUT BIT_VECTOR(7 DOWNTO 0); SIGNAL y: INOUT INTEGER RANGE 0 TO 99) IS

BEGIN

END my_procedure;

Procedure Call

Contrary to a FUNCTION, which is called as part of an expression, a PROCE-DURE call is a statement on its own. It can appear by itself or associated to a statement (either concurrent or sequential).

Examples of procedure calls:

Procedure Location



Figure 11.1 Typical locations of a FUNCTION or PROCEDURE.

Procedure Location



Figure 11.5 min_max circuit of example 11.9.

Example 11.9: PROCEDURE Located in the Main Code

```
_____
  LIBRARY ieee;
2
  USE ieee.std logic 1164.all;
3
                        ENTITY min max IS
5
   GENERIC (limit : INTEGER := 255);
6
   PORT ( ena: IN BIT;
7
        inpl, inp2: IN INTEGER RANGE 0 TO limit;
8
        min_out, max_out: OUT INTEGER RANGE 0 TO limit);
9
 END min max;
10
```

14	PROCEDURE sort (SIGNAL in1, in2: IN INTEGER RANGE 0 TO limit;
15	SIGNAL min, max: OUT INTEGER RANGE 0 TO limit) IS
16	BEGIN
17	IF (in1 > in2) THEN
18	<pre>max <= in1;</pre>
19	<pre>min <= in2;</pre>
20	ELSE
21	<pre>max <= in2;</pre>
22	<pre>min <= in1;</pre>
23	END IF;
24	END sort;
25	
26	BEGIN
27	PROCESS (ena)
28	BEGIN
29	<pre>IF (ena='1') THEN sort (inpl, inp2, min_out, max_out);</pre>
30	END IF;
31	END PROCESS;
32	END my_architecture;

Example 11.10: PROCEDURE Located in a Package

```
----- Package: -----
                                                              ----- Main code: -----
  LIBRARY ieee;
                                                           2 LIBRARY ieee;
2
  USE ieee.std_logic_1164.all;
                                                           3 USE ieee.std logic 1164.all;
3
                                                           4 USE work.my package.all;
     _____
                                                              5
  PACKAGE my package IS
                                                             ENTITY min max IS
                                                           6
     CONSTANT limit: INTEGER := 255;
6
                                                                GENERIC (limit: INTEGER := 255);
                                                           7
     PROCEDURE sort (SIGNAL in1, in2: IN INTEGER RANGE 0 TO limit;
7
                                                           8
                                                                PORT ( ena: IN BIT;
       SIGNAL min, max: OUT INTEGER RANGE 0 TO limit);
8
                                                                      inpl, inp2: IN INTEGER RANGE 0 TO limit;
                                                           9
  END my package;
                                                           10
                                                                      min out, max out: OUT INTEGER RANGE 0 TO limit);
   11 END min max;
11 PACKAGE BODY my package IS
                                                           12 -----
     PROCEDURE sort (SIGNAL in1, in2: IN INTEGER RANGE 0 TO limit;
12
                                                           13 ARCHITECTURE my architecture OF min max IS
       SIGNAL min, max: OUT INTEGER RANGE 0 TO limit) IS
13
                                                           14 BEGIN
14
     BEGIN
                                                           15
                                                                PROCESS (ena)
       IF (in1 > in2) THEN
15
                                                           16
                                                                BEGIN
          max <= in1;</pre>
                                                           17
16
                                                                   IF (ena='1') THEN sort (inpl, inp2, min out, max out);
                                                           18
                                                                   END IF;
17
          min <= in2;</pre>
                                                                END PROCESS;
                                                           19
18
       ELSE
                                                           20 END my architecture;
19
          max \leq in2;
                                                           21 -----
          min <= in1;</pre>
20
21
       END IF;
22
     END sort;
23 END my package;
```

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Example 11.10: PROCEDURE Located in a Package



Figure 11.6 Simulation results of example 11.9.

FUNCTION versus PROCEDURE Summary

- A FUNCTION has zero or more input parameters and a single return value. The input parameters can only be CONSTANTS (default) or SIGNALS (VARIABLES are not allowed).
- A PROCEDURE can have any number of IN, OUT, and INOUT parameters, which can be SIGNALS, VARIABLES, or CONSTANTS. For input parameters (mode IN) the default is CONSTANT, whereas for output parameters (mode OUT or INOUT) the default is VARIABLE.
- A FUNCTION is called as part of an expression, while a PROCEDURE is a statement on its own.
- In both, WAIT and COMPONENTS are not synthesizable.
- The possible locations of FUNCTIONS and PROCEDURES are the same (figure 11.1). Though they are usually placed in PACKAGES (for code partitioning, code sharing, and code reuse purposes), they can also be located in the main code.



The assignments will be attached to your class room.

• Problem 11.7: Statistical Procedure

End of lecture 13 Any Questions ?